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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/841,582	04/24/2001	Kazuo Nishiyama	09792909-4979	5398
33448 759	05/15/2001		EXAMINER	
ROBERT J. DEPKE LEWIS T. STEADMAN HOLLAND & KNIGHT LLC			CRUZ, LOURDES C	
131 SOUTH DE			ART UNIT	PAPER NUMBER
30TH FLOOR CHICAGO, IL	60603		2827	
511167136, 1E 00003			DATE MAIL ED: 05/19/2004	

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary		Application N	Applicant(s)	
		09/841,582	NISHIYAMA ET AL.	
		Examiner	Art Unit	
	The MAILING DATE of this and the	Lourdes (Elle) Cruz	2827	AN
Period	The MAILING DATE of this c mmunicati n app for Reply	ears n the cover sheet	with the corresp ndence address -	• '
IHE - Ex aft - If t - If t - An ea	HORTENED STATUTORY PERIOD FOR REPLY EMAILING DATE OF THIS COMMUNICATION. tensions of time may be available under the provisions of 37 CFR 1.13 er SIX (6) MONTHS from the mailing date of this communication. he period for reply specified above is less than thirty (30) days, a reply 40 period for reply is specified above, the maximum statutory period willure to reply within the set or extended period for reply will, by statute, by reply received by the Office later than three months after the mailing ned patent term adjustment. See 37 CFR 1.704(b).	36(a). In no event, however, may within the statutory minimum of Mill apply and will expire SIX (6) M	a reply be timely filed  hirty (30) days will be considered timely.  ONTHS from the mailing date of this communical	tion.
Status	Decreasing to administrative ( ) 51 July 2007			
1)[\(\sum_{2=\}[-]	· · · · · · · · · · · · · · · · · · ·			
2a)		s action is non-final.		
3) Disposi	Since this application is in condition for allowa closed in accordance with the practice under <i>l</i> tion of Claims	nce except for formal m Ex parte Quayle, 1935 (	natters, prosecution as to the merit C.D. 11, 453 O.G. 213.	s is
4)⊠	Claim(s) 1-20 is/are pending in the application			
	4a) Of the above claim(s) 10-20 is/are withdraw	n from consideration.		
5)	Claim(s) is/are allowed.			
6)⊠	Claim(s) <u>1-9</u> is/are rejected.			
7)	Claim(s) is/are objected to.			•
8) Applica	Claim(s) are subject to restriction and/or tion Papers	election requirement.		
9)	The specification is objected to by the Examiner	•		
1	The drawing(s) filed on is/are: a) accept		the Examiner	•
	Applicant may not request that any objection to the			
11)□	The proposed drawing correction filed on			. •
_	If approved, corrected drawings are required in repl			
	The oath or declaration is objected to by the Exa	miner.		
1	under 35 U.S.C. §§ 119 and 120			
	Acknowledgment is made of a claim for foreign	priority under 35 U.S.C	. § 119(a)-(d) or (f).	
( a)	☑ All b)☐ Some * c)☐ None of:			
	1. Certified copies of the priority documents			
	2. Certified copies of the priority documents			
* (	<ol> <li>Copies of the certified copies of the priorit application from the International Bure See the attached detailed Office action for a list o</li> </ol>	eau (PCT Rule 17.2(a)).	, , ,	
	Acknowledgment is made of a claim for domestic			4° \
	)  The translation of the foreign language prov			uori).
15) 🗌	Acknowledgment is made of a claim for domestic	priority under 35 U.S.C	S. §§ 120 and/or 121.	
Attachmen			• • • • • • • • • • • • • • • • • • • •	
2) Notic	e of References Cited (PTO-892) se of Draftsperson's Patent Drawing Review (PTO-948) mation Disclosure Statement(s) (PTO-1449) Paper No(s) <u>50</u> .	5) Notice of	Summary (PTO-413) Paper No(s) Informal Patent Application (PTO-152)	
U.S. Patent and T PTO-326 (Re		on Summary	Part of Paper No. 54	

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#### **DETAILED ACTION**

### Claim Rejections - 35 USC § 112

The following is a quotation of the second paragraph of 35 U.S.C. 112:

The specification shall conclude with one or more claims particularly pointing out and distinctly claiming the subject matter which the applicant regards as his invention.

Claims 1-5 are rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention.

Claim 1 recites "the side surfaces" of the semiconductor chip. See that this term lacks antecedent basis in the claim. See that claims 2-5 depend upon 1 and have therefore also been rendered unclear by this.

#### Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(e) the invention was described in-

(2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that a patent shall not be deemed filed in the United States for the purposes of this subsection based on the filing of an international application filed under the treaty defined in section 351(a).

<sup>(1)</sup> an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effect under this subsection of a national application published under section 122(b) only if the international application designating the United States was published under Article 21(2)(a) of such treaty in the English language; or

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Claims 1-4 and 6-9 are rejected under 35 U.S.C. 102(e) as being anticipated by Corisis et al. (US 5956236).

Corisis et al. discloses an electronic component (See Fig. 5 and 6) comprising:

At least one chip 12 (see multiple chips 12) having at least its electrodes (underneath 16, connected to 16) formed exclusively on one surface thereof, and surfaces other than said one surface being continuously covered with a protective material 14b.

Corisis et al also discloses the above electronic component wherein:

- Said protective material comprises an organic resin or an inorganic insulating material (Col. 3, lines 50+)
- A semiconductor chip 12 mounted on a package substrate 10, the electrode formed on a device surface of the chip, and both a side wall and a bottom surface of the chip are covered with the protective material
- Chips bonded to each other
- Chips bonded by said protective material and a solder bump 16 on the electrodes, the electrodes formed solely on one surface of the chip and wherein interspaces and bottom surfaces of the chips are continuously covered by the protective material
- A Chip array

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 Also regarding claims 1-9, see that the protective material 14b is adjacent the side surfaces of the chip and provides substantially vertical side walls of protective material.

Regarding claims 3, and 8:

A "product by process" claim is directed to the product per se, no matter how actually made, In re Brown, 173 USPQ 685; In re Luck, 177 USPQ 523; In re Fessmann, 180 USPQ 324; In re Avery, 186 USPQ 161; In re Wertheim, 191 USPQ 90 (209 USPQ 554 does not deal with this issue); In re Marosi et al, 218 USPQ 289; and particularly In re Thorpe, 227 USPQ 964, all of which make it clear that it is the patentability of the final product per se which must be determined in a "product by process" claim, and not the patentability of the process, and that an old or obvious product produced by a new method is not patentable as a product, whether claimed in "product by process" claims or not. Note that applicant has the burden of proof in such cases, as the above case law makes clear.

## Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

<sup>(</sup>a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

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Claim 5 is rejected under 35 U.S.C. 103(a) as being unpatentable over Corisis et al. (US 5956236).

See that Corisis et al. discloses all the above including plural chips. However, see that the prior art fails to specifically disclose "a plurality of **different** types of semiconductor chips". See that it is not uncommon in the art to have an arrangement of "different types" of chips in a multi module package or multi-chip package. It would have been obvious to one with ordinary skill in the art at the time the invention was made to arrange different types of chips in a package in order to economize space by arranging different types of chips within the same module instead of using separate/different modules for each different type of chip.

### Response to Arguments

Applicant has amended the claims to recite new limitations. No arguments regarding the previous rejection were filed. See rejection above where the examiner has addressed all newly submitted structural limitations in the claims.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Lourdes (Elle) Cruz whose telephone number is (571) 272-1928. The examiner can normally be reached on M-F 6:30-3:00.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Kammand Cuneo can be reached on (571) 272-1957. The fax phone numbers for the organization where this application or proceeding is assigned are (703)

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872-9306 for regular communications and (703) 872-9306 for After Final communications.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is 703-308-0956.

Lourdes (Elle) Cruz

Examiner

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Elle Cruz May 17, 2004

way 17, 2004

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